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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/660,670	09/12/2003	Josephus A.E.P. van Engelen	1875.4690000	7574
26111	7590	10/26/2005		
STERNE, KESSLER, GOLDSTEIN & FOX PLLC 1100 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005				
			EXAMINER VO, TIM T	
			ART UNIT 2112	PAPER NUMBER

DATE MAILED: 10/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/660,670	Applicant(s) VAN ENGELEN ET AL.	
	Examiner Tim T. Vo	Art Unit 2112	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-24 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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Part III DETAILED ACTION

Notice to Applicant(s)

This application has been examined. Claims 1-24 are pending.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-24 are rejected under 35 U.S.C. § 102(b) as being anticipated by Oprescu et al. patent number 5,559,967 referred hereinafter "Oprescu".

As for claim 1, Oprescu teaches a serial data interface system (see figure 19 and column 4 lines 7-11) comprising: a first transceiver configured to comply with a first standard coupled to a set of pins (see figure 19, transceiver 14 and column 17 lines 2-12, wherein the transceiver is coupling to the pins of buses 40, 41 and it is comply with differential and common mode); and a second transceiver configured to comply with a second standard coupled to the set of pins (see figure 19, transceiver 16 and column 17 lines 2-12, wherein the transceiver is coupling to the pins of buses 40, 41 and it is comply with differential and common mode).

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As for claim 2, Oprescu teaches wherein: the first standard is IEEE 1394-1995/1394a-2000 standard; and the second standard is IEEE 1394b-2002 standard (see column 2 lines 47-53).

As for claim 3, Oprescu teaches wherein the first transceiver device comprises: a twisted-wire pair (TP) bias section; a first TP transceiver section; and a second TP transceiver section (see figure 19 and column 4 lines 7-11).

As for claim 4, Oprescu teaches wherein the TP bias section comprises: a TP bias device; and a connection detection device (see figure 4 and column 3 lines 29-31).

As for claims 5-8, Oprescu teaches wherein the first TP transceiver section comprises: a strobe signal device; a data signal device; an arbitration signal device; and a speed detection device (see figure 3A).

As for claims 9-14, Oprescu teaches wherein the second transceiver comprises: a transmitter section coupled to the second pin; and a receiver section coupled to the first pin (see figures 3A, -3B, 4, 13, 19).

As for claim 15, Oprescu teaches a serial data interface system (see figure 19 and column 4 lines 7-11), comprising: a first section (first node 21) configured to comply with a first standard including, a TPBIAS device section coupled to first and second pins

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(through additional circuitry), a first transceiver section coupled to the first and second pins (see figure 19, transceiver 14, signals 40, 41), and a second transceiver section coupled to the third and fourth pins (see figure 19, transceiver 18, signals 42, 43), and a second section (second node 23) configured to comply with a second standard including, a signal transmitting device coupled to the third and fourth pins, and a signal receiving device coupled to the first and second pins (see figure 19, node 23 coupling to signals 41-43).

As for claim 16, Oprescu teaches wherein: the first standard is IEEE 1394-1995/1394a-2000 standard; and the second standard is IEEE 1394b-2002 standard (see column 2 lines 47-54).

As for claim 17, Oprescu teaches a serial data interface system, comprising: first and second pair of pins; a bias pin; a first transceiver including first and second transceiver modules, said first transceiver module coupled to said first pair of pins, said second transceiver module coupled to said second pair of pins; and a second transceiver including a transceiver module coupled to said second pair of pins and a receiver module coupled to said first pair of pins.

As for claim 18, Oprescu teaches a method comprising: (a) transmitting and receiving data in compliance with a first standard on first and second differential media pairs (see figure 19 and column 8 lines 15-29); (b) transmitting data in compliance with a second

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standard on the first differential media pair (see column 17 lines 30-42); (c) receiving data in compliance with the second standard on the second differential media pair (see column 17 lines 30-42); and (d) switching use of the first and second differential media pair between step (a) and steps (b) and (c) (see columns 17-18).

As for claim 19, Oprescu teaches wherein steps (b) and (c) are performed substantially simultaneously (see timing diagram figures 13-18).

As for claim 20, Oprescu teaches using IEEE 1394-1995/1394a-2000 as the first standard; and using IEEE 1394b-2002 as the second standard (see column 2 lines 47-54).

As for claims 21-24, Oprescu teaches wherein the first transceiver comprises: a bias section; a first transceiver section; and a second transceiver section (see figures 3A, 9).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim T. Vo whose telephone number is 571-272-3642. The examiner can normally be reached on 7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571-272-3672. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Tim Vo', is positioned above the printed name and title.

10/14/2005

Tim T. Vo
Primary Examiner
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